

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

LISTING OF CLAIMS:

Claims 1-10 (cancelled)

11. (new) A non-volatile storage device comprising:

a controller;

a buffer memory; and

a nonvolatile memory,

wherein said buffer memory comprises a plurality of banks, and

wherein said controller effects control to receive data from outside of said nonvolatile storage device, to store received data to said banks of said buffer memory, and to write received data from said banks of said buffer memory to said nonvolatile memory,

wherein said controller detects whether received data has been written from each bank of said buffer memory to said nonvolatile memory, and effects control to select, for storing further received data, one of said banks of said buffer memory that is not storing received data yet to be written to said nonvolatile memory, and

wherein said controller provides a first signal to the outside of said nonvolatile storage device when said controller detects that all of said banks of said buffer memory are storing received data yet to be written to said nonvolatile memory.

12. (new) A nonvolatile storage device according to claim 11,

wherein a total storage size of all banks of said buffer memory equals a size of a unit of data that is written into said nonvolatile memory at a time.

13. (new) A nonvolatile storage device according to claim 11, further comprising:

a status register including a state indication which indicates whether a transfer of data from said buffer memory to said nonvolatile memory has been completed, and

wherein said status flag is set by said controller.

14. (new) A nonvolatile storage device according to claim 13,

wherein said status indication includes a plurality of flags respectively corresponding to said plurality of banks of said buffer memory, and

wherein each of said flags indicates whether transferring of data from the corresponding bank of said buffer memory to said nonvolatile memory has been completed.

15. (new) A nonvolatile storage device according to claim 14, further comprising:

a first register containing information which indicates whether each of said banks of said buffer memory is storing received data, and

a second register containing information which indicates whether received data stored in each of said banks of said buffer memory has been transferred to said nonvolatile memory,

wherein said controller judges whether each of said banks of said buffer memory stores received data yet to be transferred to said nonvolatile memory, based on information contained in said first register and information contained in said second register, for setting each of said flags of said status indication.

16. (new) A nonvolatile storage device according to claim 11, further comprising:

a register in which an address range of said nonvolatile memory is set; and

a data storage error detection circuit which detects whether data may be written to said nonvolatile memory, based on the address information that is set in said register and an amount of data that is received from the outside.